

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
100V	40mΩ@10V	17A
	49mΩ@4.5V	

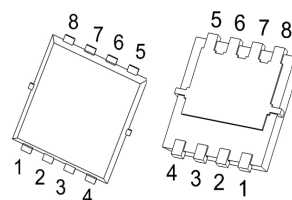
Feature

- Split Gate Trench Technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

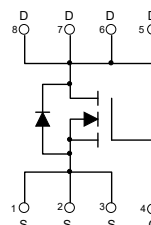
Application

- Power Management
- Load Switching

PDFN3.3X3.3-8L



Schematic diagram



Package Marking and Ordering Information

Part Number	Package	Marking	Packing	Reel Size	Tape Width	Qty
GPT500N10LNA	PDFN3.3X3.3-8L	T500N10L	Reel & Tape	330mm	12mm	5000pcs

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current ¹	$T_C = 25^{\circ}\text{C}$	I_D	17	A
	$T_C = 100^{\circ}\text{C}$		10.5	
Pulsed Drain Current ²		I_{DM}	68	A
Single Pulsed Avalanche Current ³		I_{AS}	10	A
Single Pulsed Avalanche Energy ³		E_{AS}	5	mJ
Power Dissipation ⁵		P_D	28	W
$T_C = 25^{\circ}\text{C}$				
Thermal Resistance from Junction to Ambient ⁶		$R_{\theta JA}$	60	$^{\circ}\text{C/W}$
Thermal Resistance from Junction to Case		$R_{\theta JC}$	4.5	$^{\circ}\text{C/W}$
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~ +150	$^{\circ}\text{C}$

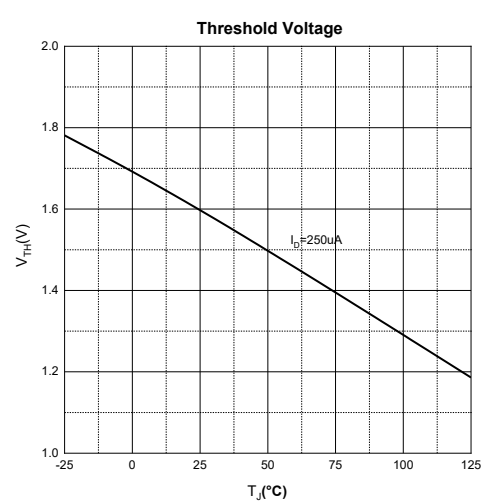
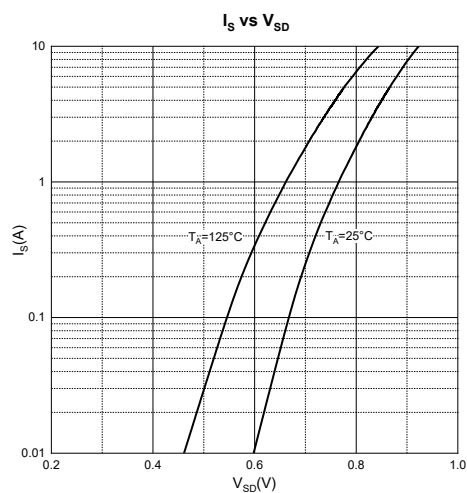
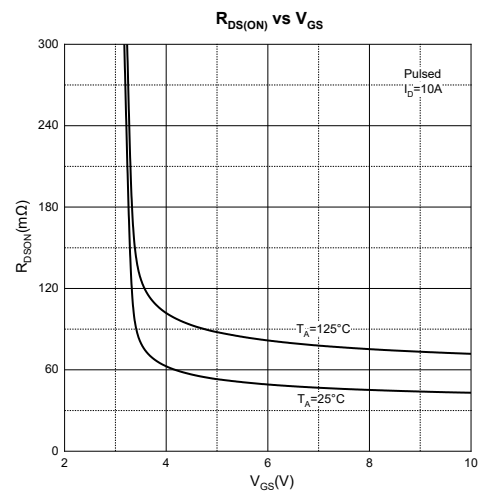
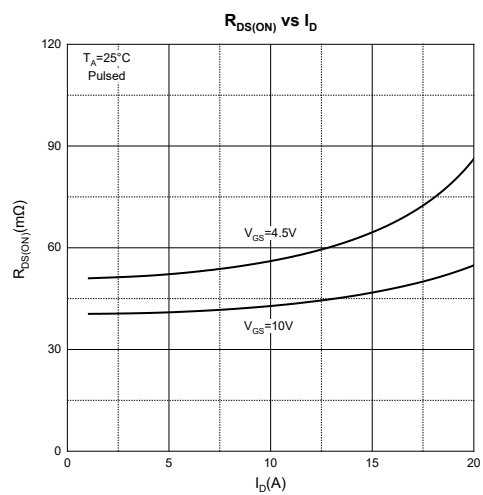
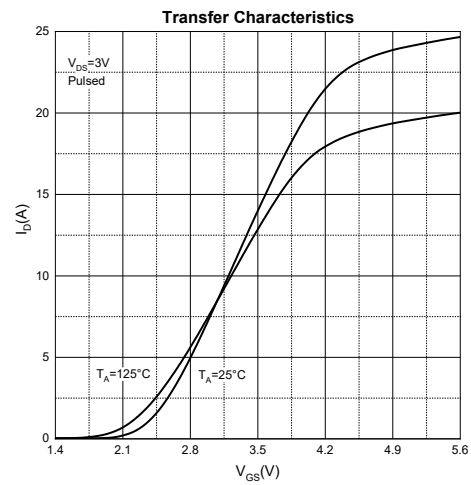
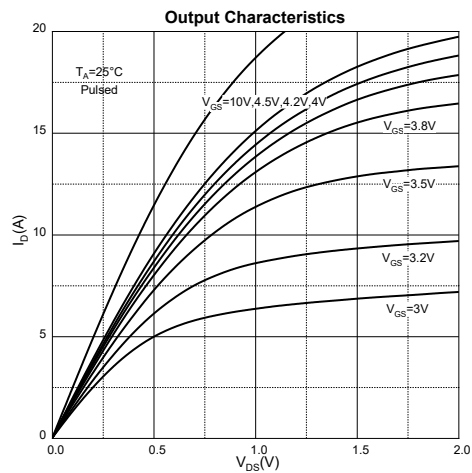
MOSFET ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

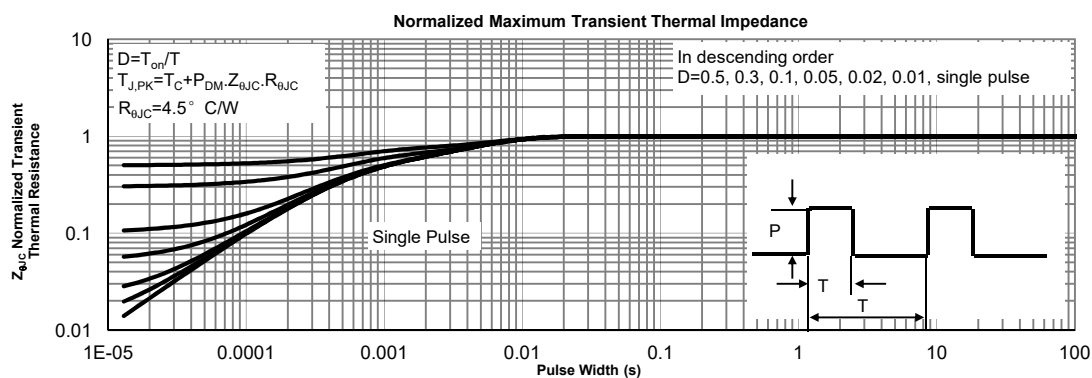
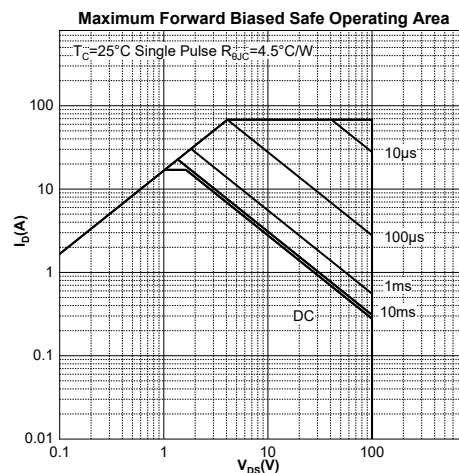
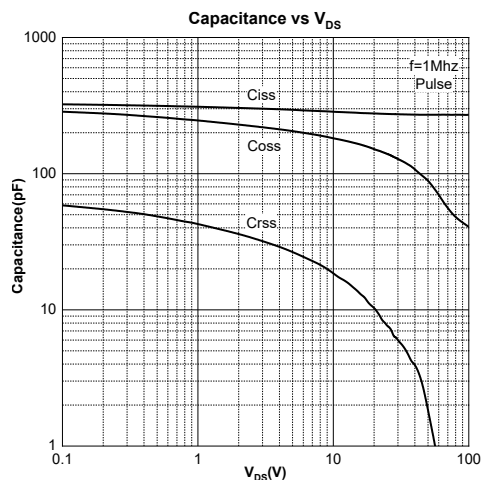
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
On Characteristics ⁴						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.6	3	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 10A		40	60	mΩ
		V _{GS} = 4.5V, I _D = 3A		49	75	
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz		272		pF
Output Capacitance	C _{oss}			91		
Reverse Transfer Capacitance	C _{rss}			2.1		
Gate Resistance	R _g	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz		1.6		Ω
Switching Characteristics						
Total Gate Charge	Q _g	V _{DS} = 30V, V _{GS} = 10V, I _D = 10A		5.8		nC
Gate-Source Charge	Q _{gs}			1.0		
Gate-Drain Charge	Q _{gd}			1.4		
Gate Plateau Voltage	V _{plateau}			3.0		V
Turn-On Delay Time	t _{d(on)}	V _{DD} = 50V, V _{GS} = 10V, R _G = 3Ω, I _D = 10A		4.1		ns
Turn-On Rise Time	t _r			4.3		
Turn-Off Delay Time	t _{d(off)}			8.7		
Turn-Off Fall Time	t _f			3.7		
Source-Drain Diode Characteristics						
Diode Forward Voltage ⁴	V _{SD}	V _{GS} = 0V, I _S = 10A			1.2	V
Continuous Source Current	I _S	T _C = 25°C			17	A
Diode Reverse Recovery Time	t _{rr}	I _F = 10A, dI/dt = 100A/μs		26		ns
Diode Reverse Recovery Charge	Q _{rr}			19		nC

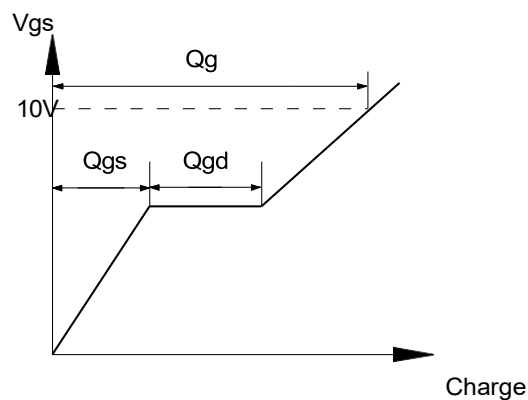
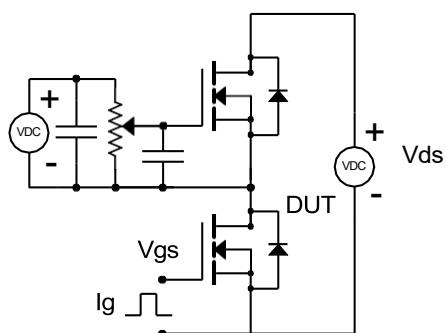
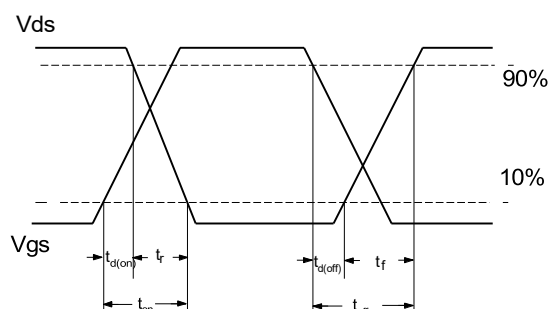
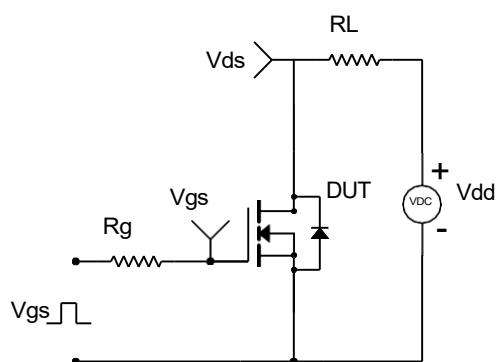
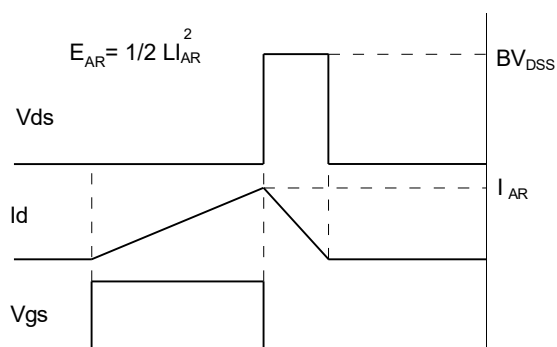
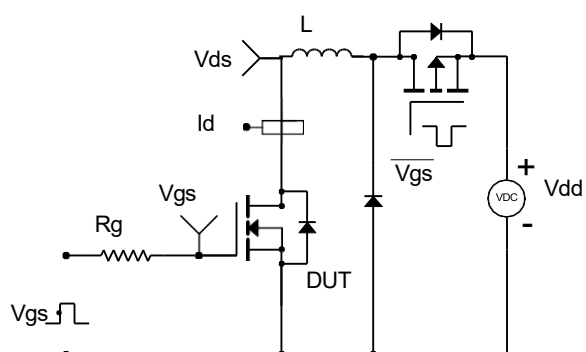
Notes:

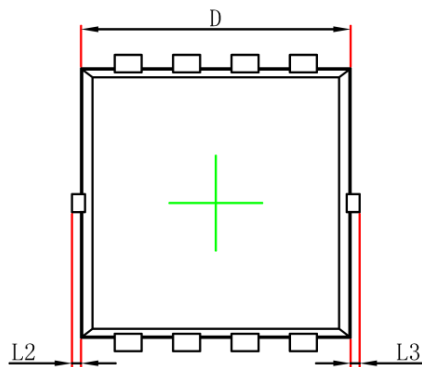
1. The maximum current rating is limited by package. And device mounted on a large heatsink.
2. Pulse Test: Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
3. EAS condition: $V_{DD} = 100V, V_{GS} = 10V, L = 0.1mH, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
4. Pulse Test: Pulse Width $\leq 1000\mu s$, duty cycle $\leq 2\%$.
5. The power dissipation P_D is limited by $T_{J(MAX)} = 150^\circ\text{C}$. And device mounted on a large heatsink.
6. Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

Typical Characteristics

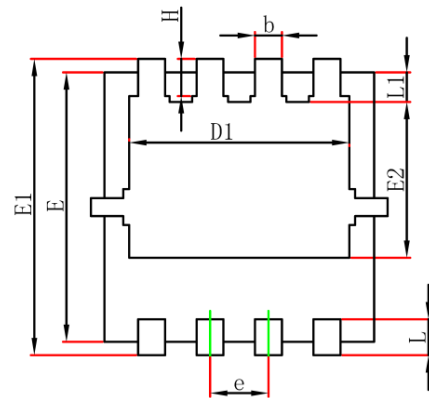




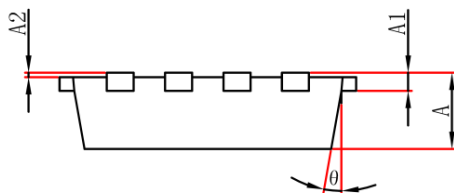
Test Circuit
Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveform

Unclamped Inductive Switching (UIS) Test Circuit & Waveforms


PDFN3.3X3.3-8L Package Information


Top View
[顶视图]



Bottom View
[背视图]



Side View
[侧视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035
A1	0.10	0.25	0.004	0.010
A2	0.000	0.050	0.000	0.002
D	2.900	3.200	0.114	0.126
D1	2.300	2.600	0.091	0.102
E	2.900	3.200	0.114	0.126
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0.000	0.100	0.000	0.004
L3	0.000	0.100	0.000	0.004
H	0.315	0.515	0.012	0.020
θ	0°	12°	0°	12°

Attention:

- GreenPower Electronics reserves the right to improve product design function and reliability without notice.
- Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.
- GreenPower Electronics products belong to consumer electronics or other civilian electronic products.